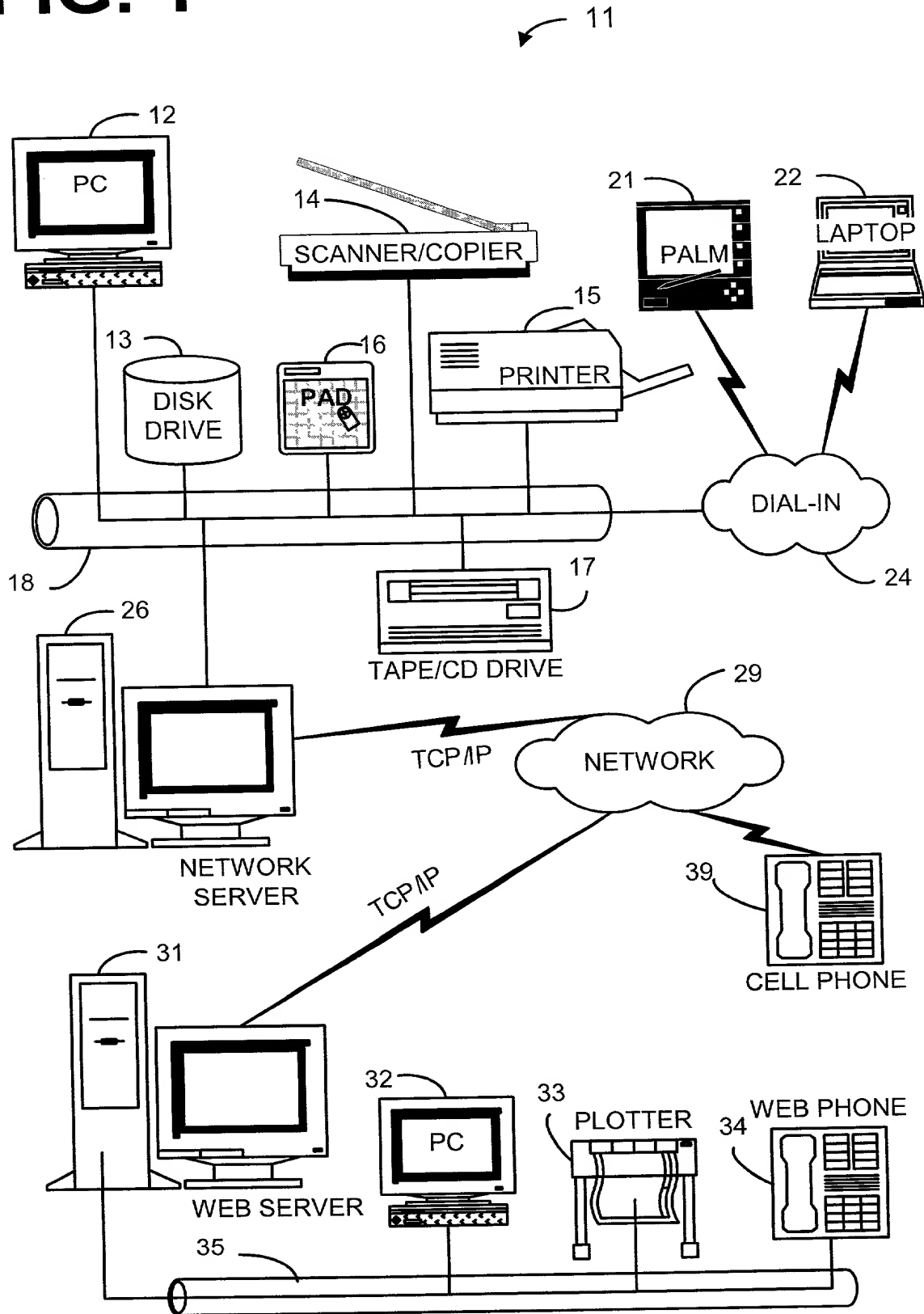


# FIG. 1

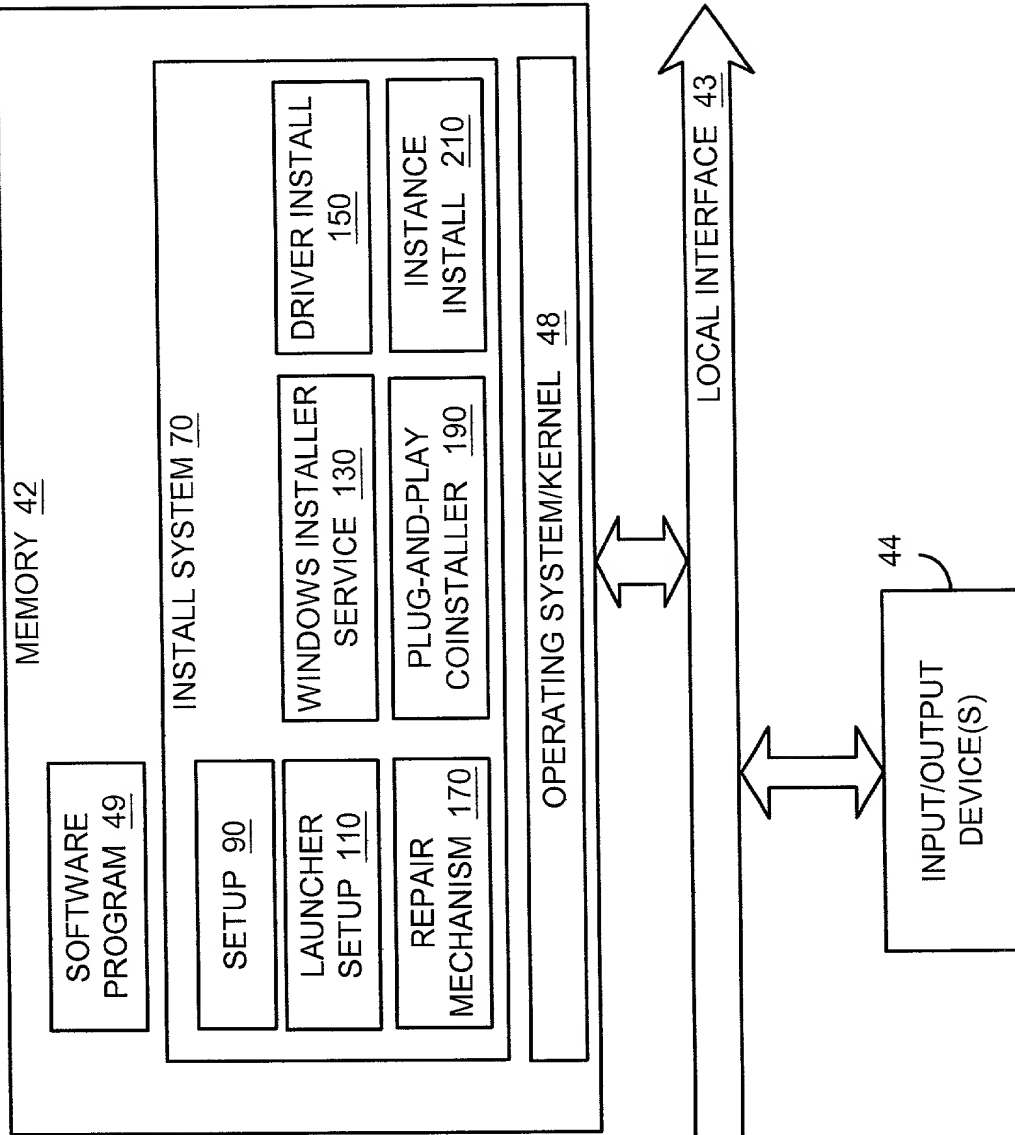


# FIG. 2

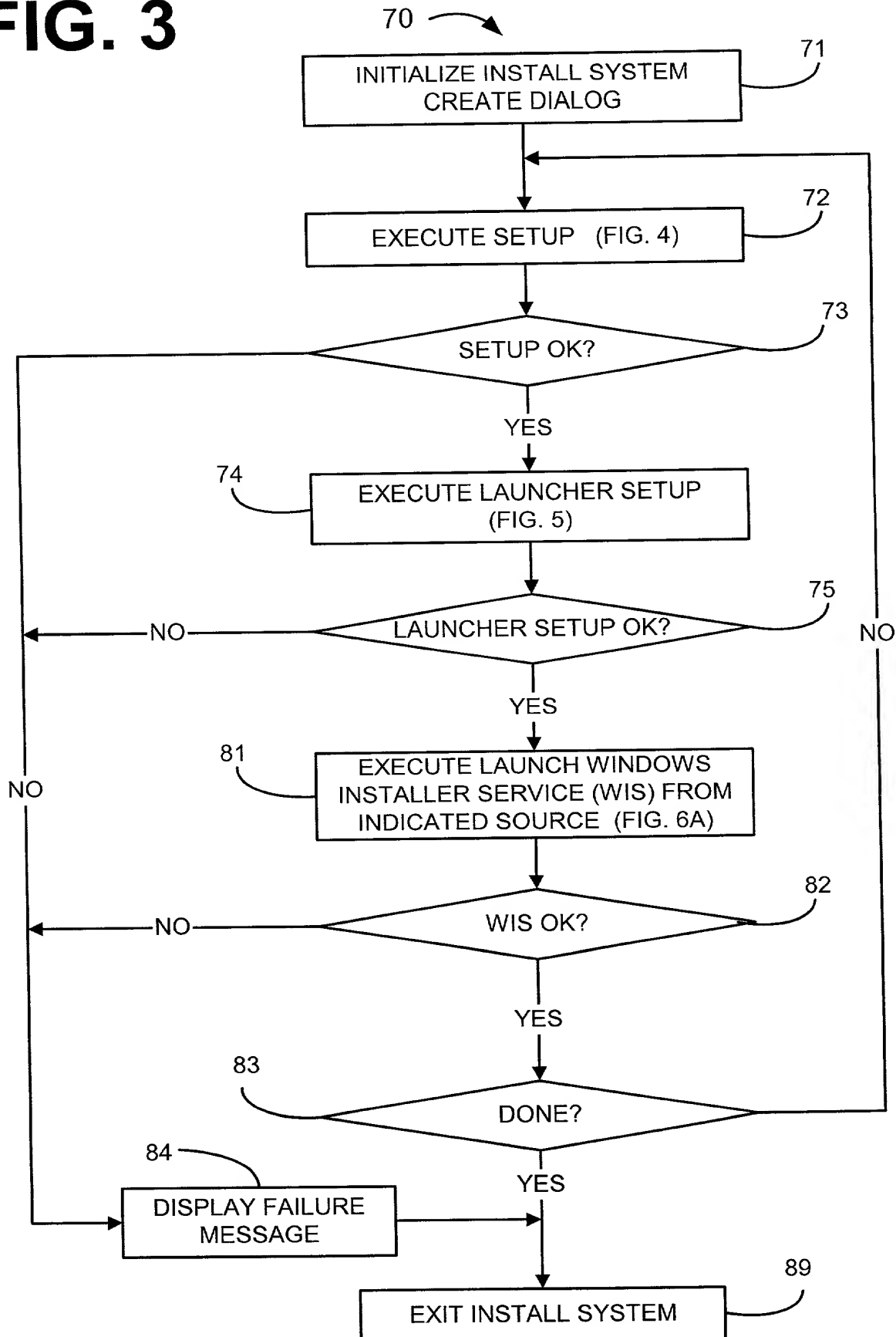
12-17, 21, 22, 26,  
31-34 AND 39

41

PROCESSOR



**FIG. 3**



**FIG. 4**

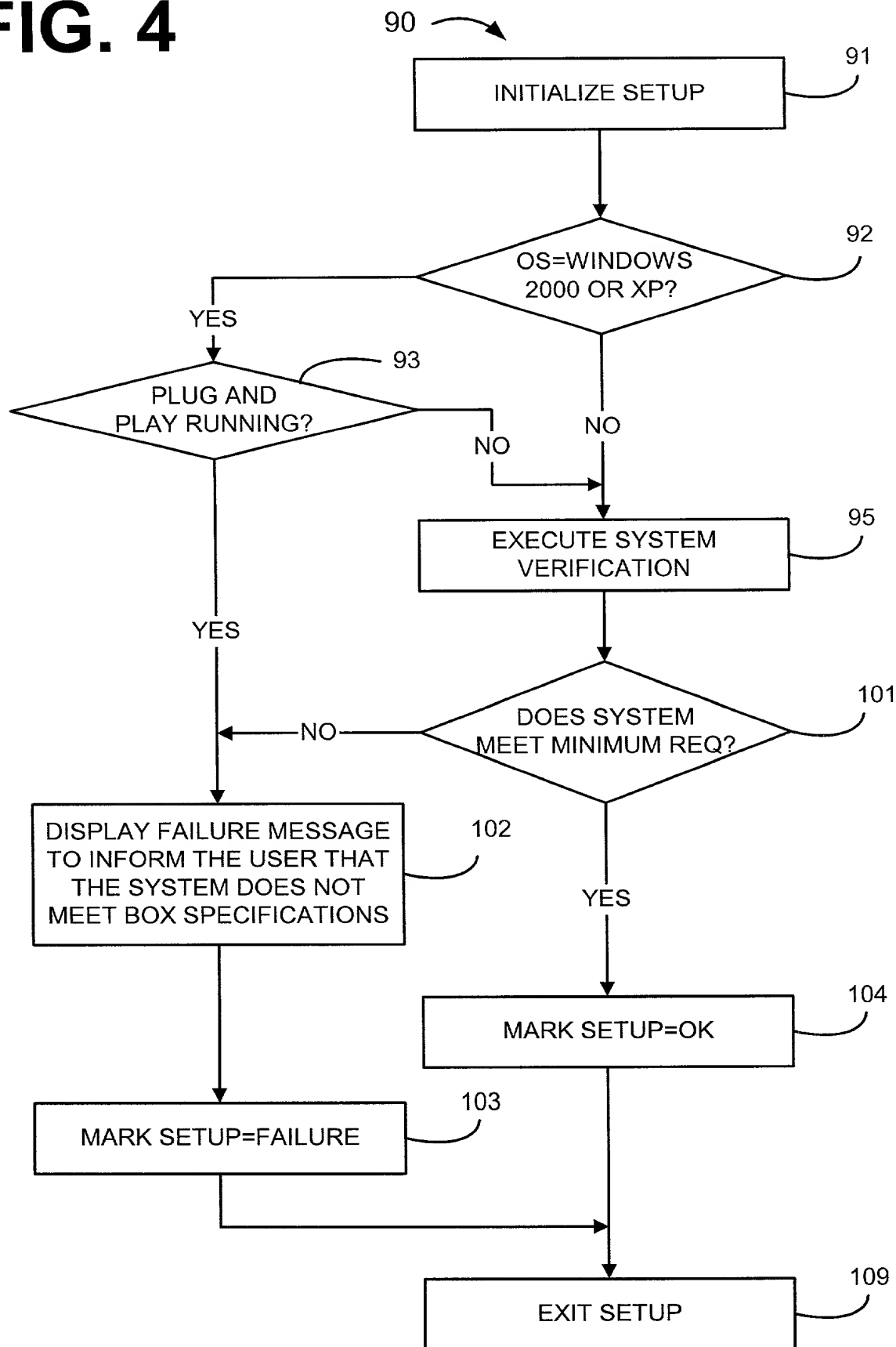


FIG. 5

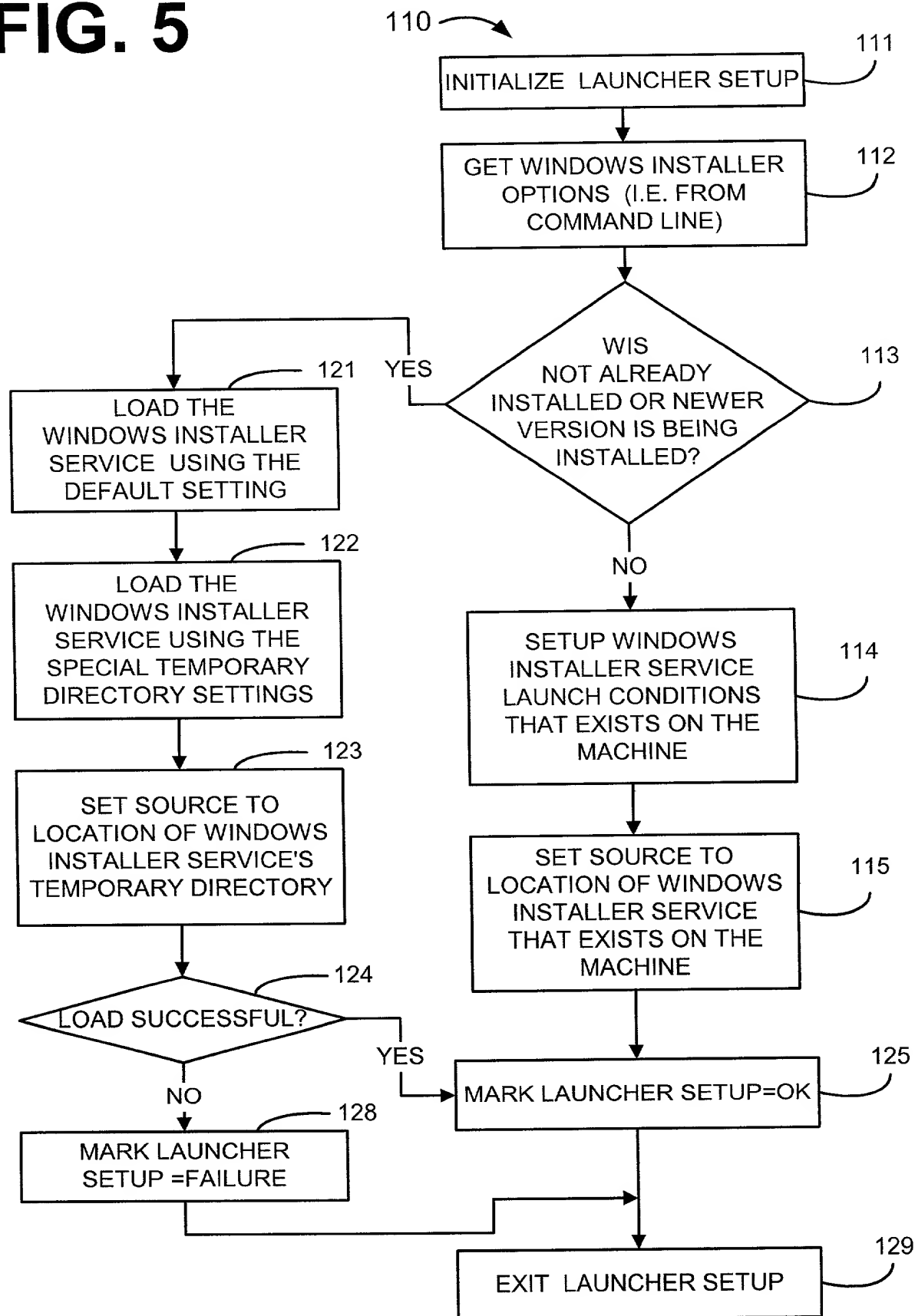
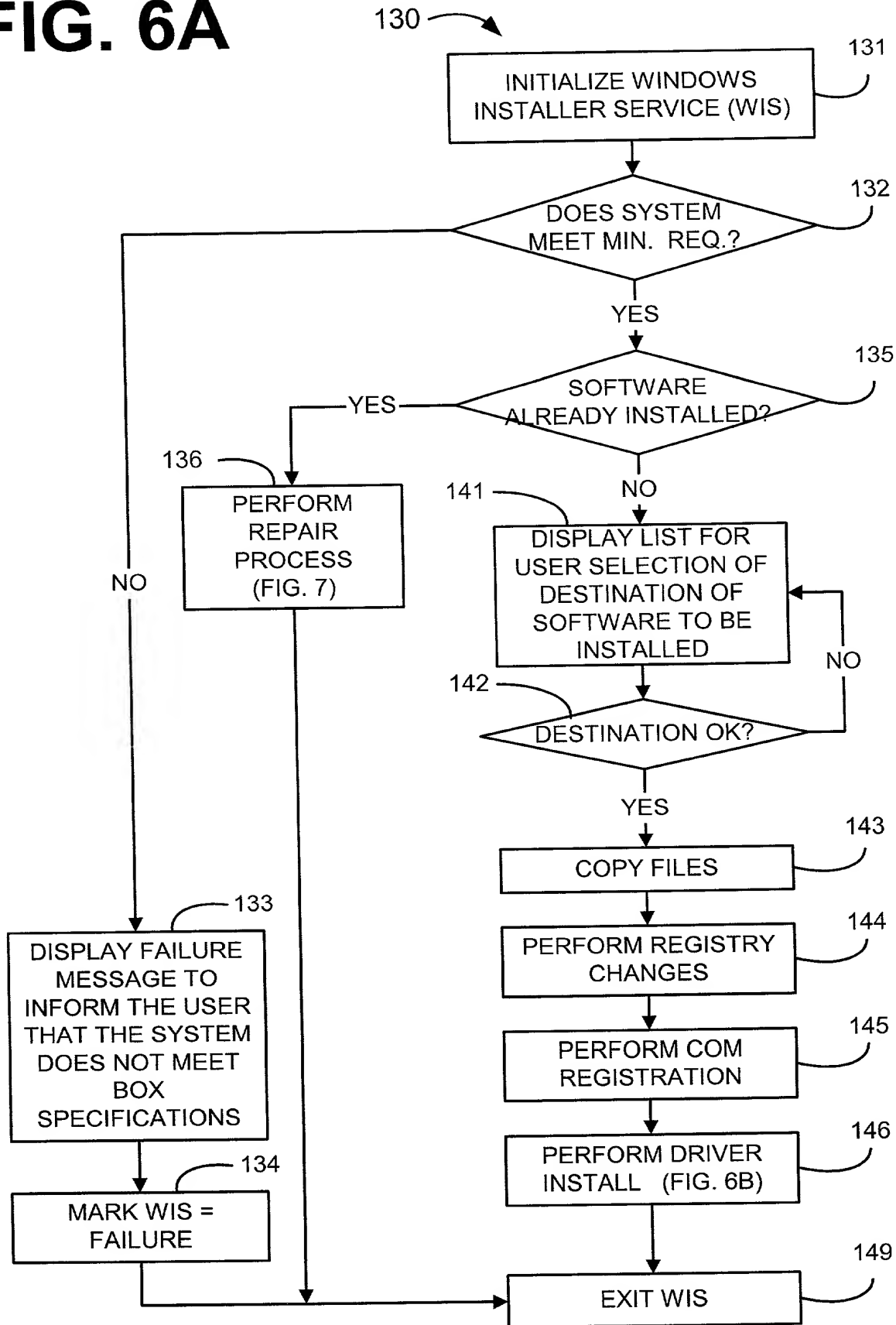


FIG. 6A



**FIG. 6B**

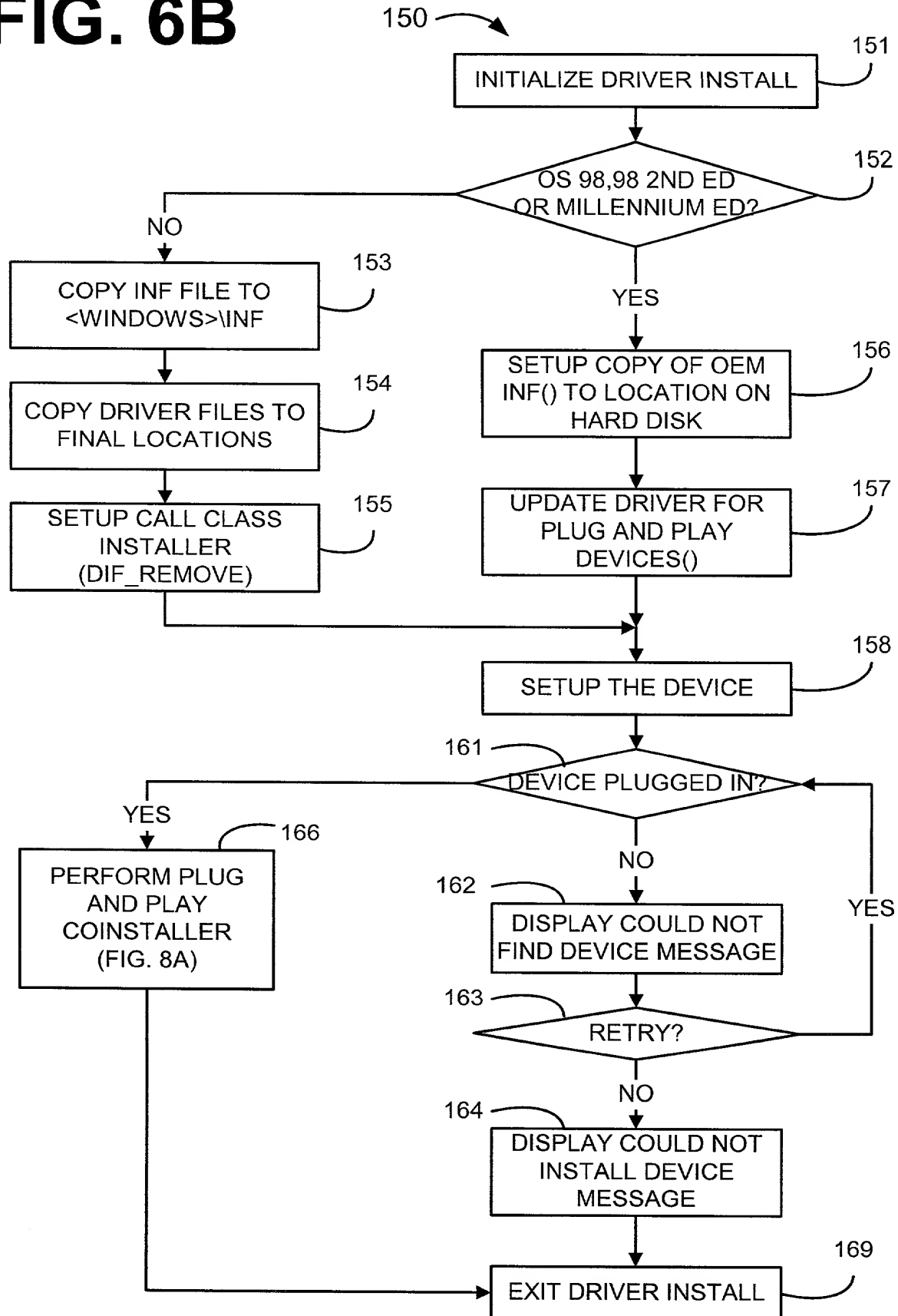
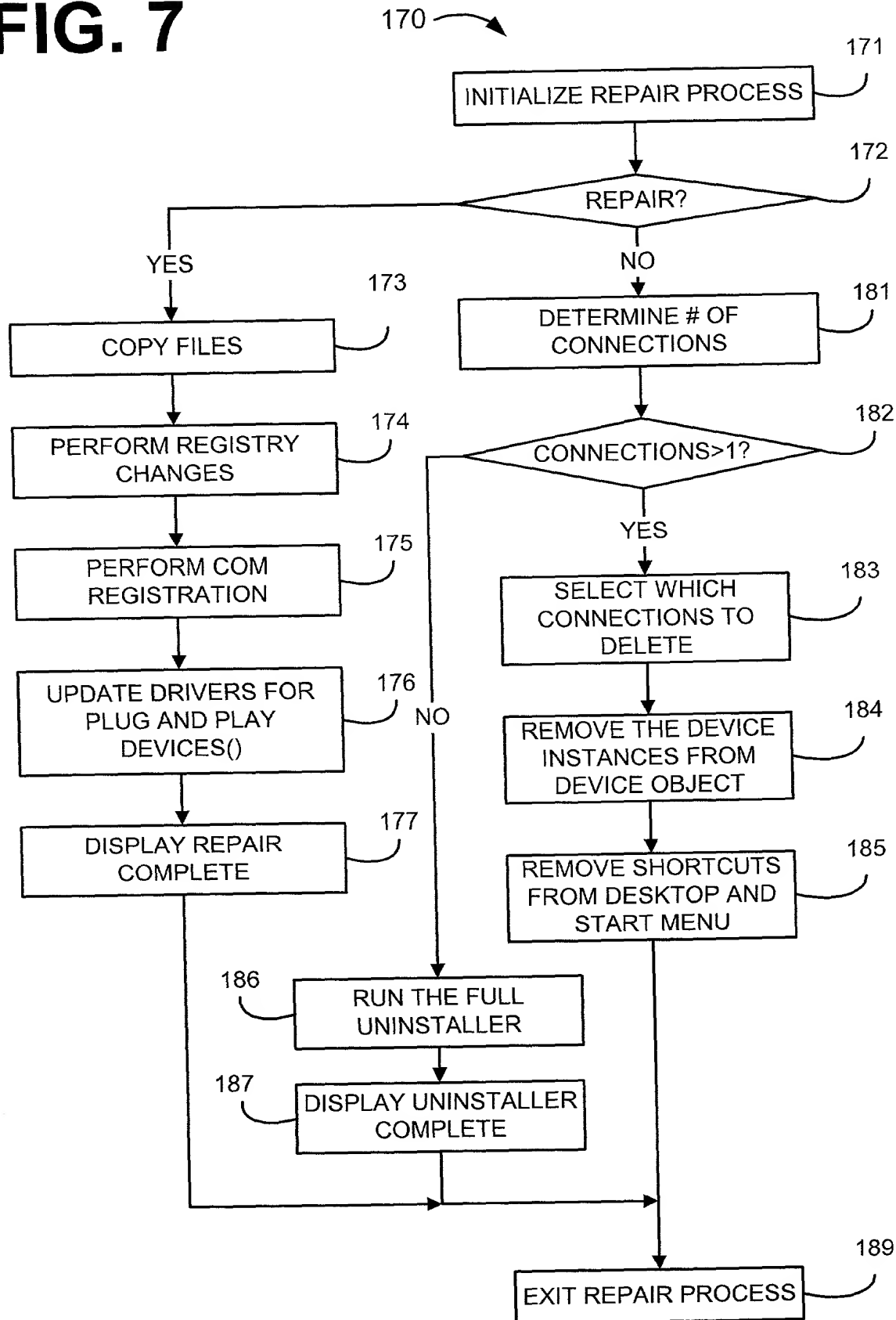
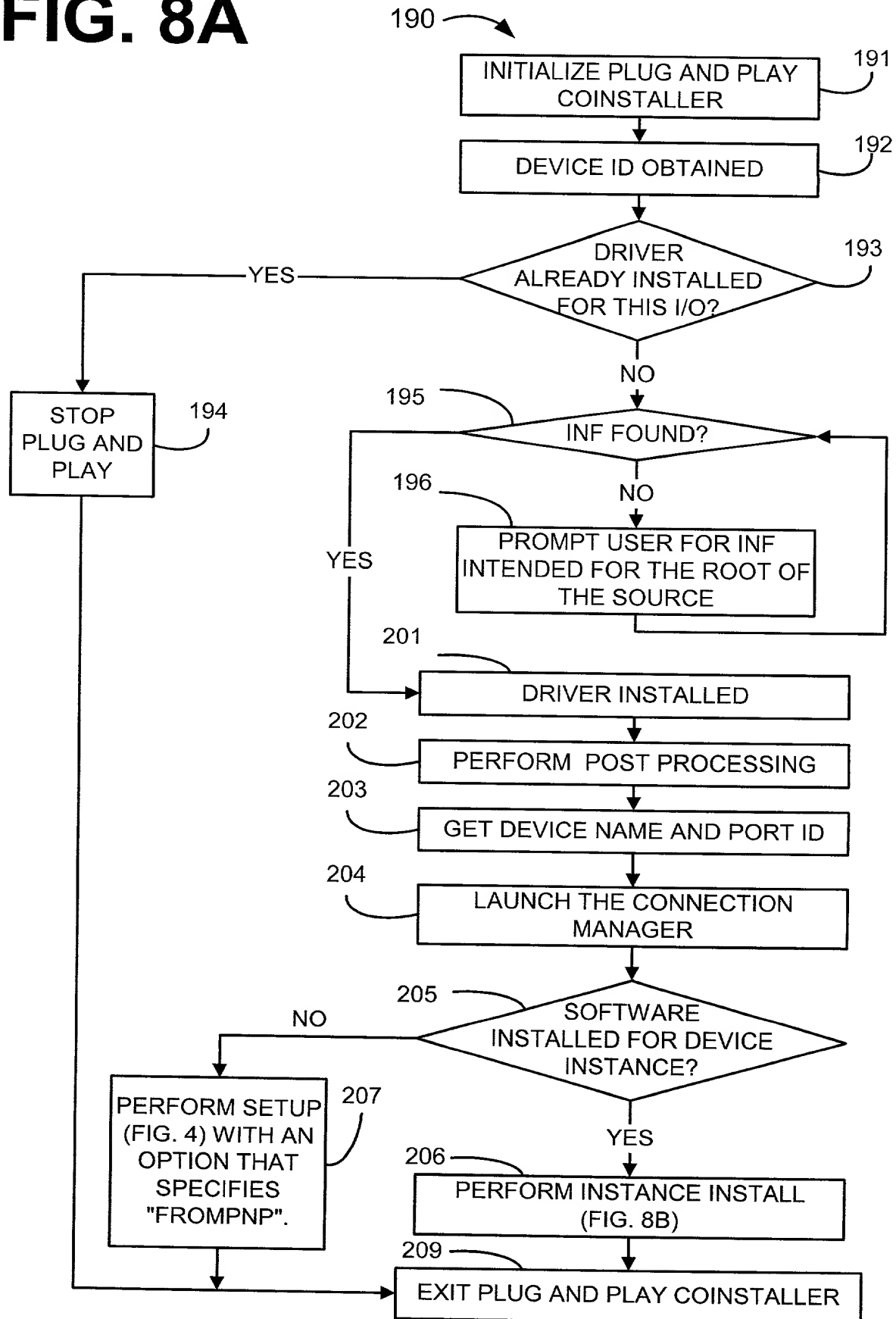


FIG. 7





# FIG. 8A



## FIG. 8B

